

What is claimed is:

1. An amplifier module comprising:  
an amplifier circuit for amplifying an input signal to  
5 generate an output signal; and  
an adaptive bias circuit for receiving the input  
signal to provide a driving current to the amplifier circuit  
for controlling a quiescent current of the amplifier circuit.
- 10 2. The amplifier module of claim 1, wherein the adaptive  
bias circuit includes:  
a driving transistor for receiving a driving  
transistor input current to provide the driving current to  
the amplifier circuit; and  
15 a drawing transistor for drawing a bypass current from  
the driving transistor input current to reduce the driving  
current in response to the input signal.
- 20 3. The amplifier module of claim 2, wherein the quiescent  
current is reduced when the driving current is reduced and  
the bypass current increases when the input signal is  
reduced.
- 25 4. The amplifier module of claim 3, wherein the adaptive  
bias circuit further includes an adjusting transistor for  
receiving the input signal to adjust a control voltage in

response to the input signal, wherein the drawing transistor draws the bypass current in response to the control voltage.

5. The amplifier module of claim 4, wherein the bypass  
5 current increases when the control voltage increases.